



IFW

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Ichio YUDASAKA et al.

Group Art Unit: 2826

Application No.: 10/717,503

Examiner: Tuan N. Quach

Filed: November 21, 2003

Docket No.: 117849

For: TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In reply to the February 18, 2005 Restriction Requirement, Applicants provisionally elect Group I, claims 1-8 and 15, drawn to a semiconductor device, classified in class 257, subclass 288.

Applicants respectfully note that claim 15 is dependent from claim 2 and, as such, should be examined with Group I. Specifically, claim 15 is drawn to a semiconductor device, and more specifically, the transistor according to claim 2. Claim 15 is not drawn to a method for making a semiconductor device, as is asserted in the Restriction Requirement. Applicants, therefore, respectfully submit that Group I should include claims 1-8 and 15.

Respectfully submitted,


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